

Appl. No. 10/708,199  
Amtd. dated July 05, 2005  
Reply to Office action of April 19, 2005

**REMARKS/ARGUMENTS**

1. Rejection of claims 10, and 13-15 under 35 U.S.C. 102(b) as being anticipated by Habermehl et al. (US 6,174,820):

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Claim 10 has been amended to overcome this rejection. Specifically, the limitation of "an insulating substrate selected from the group consisting of glass and quartz" is added to claim 10. This limitation finds support in original claims 16 and 18, in the specification in paragraph [0017] for 10 instance, and no new matter is entered. The amended claim 10 is listed thereafter for reference.

Claim 10 (currently amended) A capacitive semiconductor pressure sensor comprising:

- 15 an insulating substrate selected from the group consisting of glass and quartz;  
a conductive movable diaphragm;  
a supporter positioned on the insulating substrate for fixing two ends of the diaphragm and forming a sealed cavity between the diaphragm and the 20 insulating substrate;  
a stationary electrode positioned on the insulating substrate and below the diaphragm; and  
a control circuit electrically connected to the diaphragm and the stationary electrode.

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Regarding US 6,174,820, Habermehl discloses a MEMS device including:

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a monocrystalline silicon wafer 12;  
a hinged member 110;  
a supporter 66;  
stationary electrodes 112; and  
5 electric circuitry 54.

In Habermehl's teaching, the MEMS device is formed on a monocrystalline silicon wafer, which may be insulating if not doped. However, Habermehl fails to teach or suggest that the monocrystalline 10 silicon wafer can be a glass substrate or a quartz substrate. Therefore, claim 10 is patentably distinct from Habermehl's teaching.

In addition, the Examiner also mentioned that Lin (US 6,642,593) teaches a substrate 20 (Fig.1h) which can be made of other microwave 15 quality substrate such as a quartz or sapphire substrate, and asserts that it would have been obvious to one having ordinary skill in the art at the time of the invention was made to form a glass substrate or a quartz substrate in Habermehl's device. However, the applicant disagrees with that for the following reason. In Lin's teaching, the substrate 20 includes an insulator 20 layer 30 such as a silicon dioxide layer, and the electrode layer 40 is formed on the insulator layer 30, instead of on the substrate 20. On the contrary, the stationary electrode 40 of claim 10 is positioned directly on the glass substrate or the quartz substrate 32. Thus, the applicant believes the amended claim 10 is patentably distinct from Habermehl's teaching, 25 and it would not have been obvious to one having ordinary skill in the art to form the glass substrate or the quartz substrate in Habermehl's device. Claims 13-15 are dependent on claim 10 and should be allowed if claim 10 is allowed.

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Reconsideration of claims 10, and 13-15 is therefore respectfully requested.

5 2. Rejection of claims 10, and 14-15 under 35 U.S.C. 102(b) as being anticipated by Scheiter et al. (US 6,140,689):

Claim 10 has been amended to overcome this rejection. Specifically, the limitation of "an insulating substrate selected from the group consisting of glass and quartz" is added to claim 10. This limitation finds support in original claims 16 and 18, in the specification in paragraph [0017] for instance, and no new matter is entered. The amended claim 10 is listed thereafter for reference.

- 15 Claim 10 (currently amended) A capacitive semiconductor pressure sensor comprising:  
an insulating substrate selected from the group consisting of glass and quartz;  
a conductive movable diaphragm;  
20 a supporter positioned on the insulating substrate for fixing two ends of the diaphragm and forming a sealed cavity between the diaphragm and the insulating substrate;  
a stationary electrode positioned on the insulating substrate and below the diaphragm; and  
25 a control circuit electrically connected to the diaphragm and the stationary electrode.

Regarding US 6,140,689, Scheiter discloses a MEMS sensor including:

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- a SOI substrate (col.2, lines 50-53);  
a membrane 7 (col.2 lines 58-60);  
a spacer layer 4 (col. 2 lines 55-57);  
a cavity 6 located in the spacer layer 4 (col.2 lines 57-58);  
5 a doped region 8 (col.3 lines 3-9); and  
a MOSFET 11 (col.3 lines 26-29)

In Scheiter's teaching, the MEMS sensor is formed on a SOI substrate. Accordingly, the manufacture process of Scheiter's MEMS sensor is more complicated than that of the present application. For instance, a recess 9 must be formed on the back surface of the SOI substrate, and manufacture costs are higher. On the contrary, the pressure sensor of the present application has a simpler structure and simpler processes. Most importantly, Scheiter fails to teach or suggest that the SOI substrate can be a glass 15 substrate or a quartz substrate. Therefore, the present application is patentably distinct from Scheiter's teaching.

The Examiner also mentioned that Lin (US 6,642,593) teaches a substrate 20 (Fig.1h) which can be made of other microwave quality 20 substrate such as a quartz or sapphire substrate, and asserts that it would have been obvious to one having ordinary skill in the art at the time of the invention was made to form a glass substrate or a quartz substrate in Scheiter's pressure sensor. However, the applicant disagrees and asserts that it would not have been obvious to one having ordinary skill in the art 25 to incorporate Lin's teaching into Scheiter's teaching. In Lin's teaching, the substrate 20 requires an insulator layer 30 such as a silicon dioxide layer, and the electrode layer 40 is formed on the insulator layer 30, rather than on the substrate 20. Also, Scheiter's MEMS sensor is also formed on

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an insulating layer 2 of a SOI substrate. On the other hand, the stationary electrode 40 of the present application is directly positioned on the glass substrate or the quartz substrate 32. Thus, the applicant believes the amended claim 10 is patentably distinct from Habermehl's teaching, and it 5 would not have been obvious to one having ordinary skill in the art to implement the present application in view of Scheiter's MEMS sensor and Lin's teaching. Claims 14-15 are dependent on claim 10 and should be allowed if claim 10 is allowed.

10 Reconsideration of claims 10, and 14-15 is therefore respectfully requested.

3. Rejection of claims 20-21 under 35 U.S.C. 103(a) as being unpatentable over Habermehl et al. (US 6,174,820) in view of 15 Shrauger (US 2003/0020094):

Claims 20-21 are dependent on claim 10 and should be allowed if claim 10 is allowed. Reconsideration of claims 20-21 is therefore respectfully requested.

20 4. Rejection of claims 20-21 under 35 U.S.C. 103(a) as being unpatentable over Scheiter et al. (US 6,140,689) in view of Shrauger (US 2003/0020094):

25 Claims 20-21 are dependent on claim 10 and should be allowed if claim 10 is allowed. Reconsideration of claims 20-21 is therefore respectfully requested.

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5. **Rejection of claims 1,3, 5-15, 17, 19 and 22 under 35 U.S.C. 103(a) as being unpatentable over Guo et al. (US 6,472,962) in view of Habermehl et al. (US 6,174,820), and further in view of Bhattacharyya (US 6,845,034):**

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Claim 1 has been amended to overcome this rejection. Specifically, the limitation of "a non-single-crystal-silicon-based substrate selected from the group consisting of glass and quartz" is added to claim 1. This limitation finds support in original claims 2 and 4, in the specification in paragraph [0017] for instance, and no new matter is entered. The amended claim 1 is listed thereafter for reference.

Claim 1 (currently amended) A capacitive semiconductor pressure sensor comprising:

- 15 a non-single-crystal-silicon-based substrate selected from the group consisting of glass and quartz;
- a conductive movable polysilicon diaphragm;
- a polysilicon supporter positioned on the non-single-crystal-silicon-based substrate for fixing two ends of the polysilicon diaphragm and forming a sealed cavity between the polysilicon diaphragm and the non-single-crystal-silicon-based substrate;
- 20 a stationary electrode positioned on the non-single-crystal-silicon-based substrate and below the polysilicon diaphragm, the stationary electrode and the polysilicon diaphragm constituting a plate capacitor; and
- 25 a thin film transistor (TFT) control circuit positioned on the non-single-crystal-silicon-based substrate and electrically connected to the plate capacitor.

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Regarding US 6,472,962, Guo discloses an LCR-RF switching device including:

- 5 a substrate 80 which may be a semiconductor wafer comprising monocrystalline silicon, and a dielectric material such as silicon dioxide or a low-k material (col.5, lines 66-67);  
a membrane 122 (col.6 lines 36-39);  
posts 118 (col.7 lines 13-14);  
an up electrode 100 (col.6 lines 25-36); and  
an air gap 123 between the membrane 122 and the up electrode 100  
10 (col.6, lines 47-50).

Regarding US 6,174,820, Habermehl discloses a hinged member 110 and a supporter made of polysilicon (col. 13 lines 19-36).

15 Regarding US 6,845,034, Bhattacharyya shows a TFT control circuit 1804 positioned on the substrate and electrically connected to the plate capacitor (MEMS) 1830.

The Examiner asserts that it would have been obvious to combine Guo,  
20 Habermehl, and Bhattacharyya to form the pressure sensor of the present application. However, the applicant disagrees and asserts that it would not have been obvious to one having ordinary skill in the art to form the pressure sensor of amended claim 1. First, Guo discloses an LCR-RF switching device having a MEMS capacitor. The MEMS capacitor of the  
25 switching device has an UP state and a DOWN state. If voltage potential between the down electrode 92 and the membrane 122 is less than the minimum activation voltage, the MEMS capacitor is in the UP state. Under a contrary condition, the MEMS capacitor is in the DOWN state. It can be

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seen that MEMS capacitor of Guo is functionally and operationally different from the pressure sensor of the present application. In addition, the limitation of "a non-single-crystal-silicon-based substrate selected from the group consisting of glass and quartz" has been added to claim 1,  
5 and none of the cited art discloses a glass substrate or a quartz substrate. Therefore, it would not have been obvious to one having ordinary skill in the art to form the pressure sensor of claim 1 of the present application.

For the same reason, it would not have been obvious to one having  
10 ordinary skill in the art to implement the pressure sensor of claim 10 in view of Guo, Habermehl, and Bhattacharyya

In addition, although the Examiner mentioned that Lin (US 6,642,593) teaches a substrate 20 (Fig.1h) which can be made of other microwave  
15 quality substrate such as a quartz or sapphire substrate, and asserts that it would have been obvious to one having ordinary skill in the art at the time of the invention was made to form a glass substrate or a quartz substrate in Guo's teaching. However, the applicant disagrees and asserts that it would not have been obvious to one having ordinary skill in the art to incorporate  
20 Lin's teaching into Guo's teaching. In Lin's teaching, the substrate 20 includes an insulator layer 30 such as a silicon dioxide layer, and the electrode layer 40 is formed on the insulator layer 30, rather than on the substrate 20. On the other hand, the stationary electrode 40 of the present application is directly positioned on the glass substrate or the quartz  
25 substrate 32. Thus, the applicant believes the claims 1 and 10 would not have been obvious to one having ordinary skill in the art to implement the pressure sensor of the present application in view of Guo's teaching and Lin's teaching. Therefore, reconsideration of claims 1 and 10 is

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respectfully requested.

Regarding claims 9 and 22, none of the cited arts including Guo, Habermehl, and Bhattacharyya teaches a thin film transistor display region for displaying a variation of pressure. Therefore, it would not have been obvious to one having ordinary skill in the art to form a thin film transistor display region in the pressure sensor in view of Guo, Habermehl, and Bhattacharyya. Thus, reconsideration of claims 9 and 22 is respectfully requested.

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Claims 3 and 5-8 are dependent on claim 1 and should be allowed if claim 1 is allowed. Reconsideration of claims 3, and 5-8 is therefore respectfully requested.

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Claims 11-15, 17 and 19 are dependent on claim 10 and should be allowed if claim 10 is allowed. Reconsideration of claims 11-15, 17 and 19 is therefore respectfully requested.

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6. Rejection of claims 2, 4, 16 and 18 under 35 U.S.C. 103(a) as being unpatentable over Guo et al. (US 6,472,962) in view of Habermehl et al. (US 6,174,820), and Bhattacharyya (US 6,845,034), as applied to claims 1, 10 above and further in view of Lin et al. (US 6,642,593):

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Claims 2, 4, 16 and 18 are cancelled.

7. Rejection of claims 20-21 under 35 U.S.C. 103(a) as being unpatentable over Guo et al. (US 6,472,962) in view of Habermehl

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**et al. (US 6,174,820) and Bhattacharyya (US 6,845,034), as applied to claim 10 above and further in view of Shrauger (US 2003/0020094):**

5       Claims 20-21 are dependent on claim 10 and should be allowed if claim 10 is allowed. Reconsideration of claims 20-21 is therefore respectfully requested.

10      Applicant respectfully requests that a timely Notice of Allowance be issued in this case.

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Respectfully submitted,

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Date: July 5, 2005

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